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In re Application of

Shadi A. AbuGhazaleh et al. : Group Art Unit: 2841

Serial No.: 10/791,291 : Examiner: Not Yet Assigned

Filed: March 3, 2004

For: Midspan Patch Panel With Circuit

Separation For Data Terminal Equipment, Power Insertion And Data Collection

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97-1.98, Applicants hereby transmit the attached Form PTO-1449, which lists U.S. and foreign documents that may be deemed material to the examination of the above-identified application.

The present Information Disclosure Statement is being filed prior to issuance of a first Office Action on the merits and therefore, no certification under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(b) is required. Should any fees be required, the Director is hereby authorized to charge the fee to Deposit Account No. 18-2220.

The Examiner is requested to consider the attached documents in connection with the above-identified application and to return a copy of Form PTO-1449 to the Applicants with the Examiner's initials in the spaces provided.

Respectfully submitted,

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Dated: November 1, 2004

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Form PTO-1449 U.S. TERARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

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